 **			SHEET 1 OF
U.S. DEPARTMENT OF COMMERC PATENT AND TRADEMARK OFFICE		APPLICATION NO. 09/037,945	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT JUN 1 5 199	S APPLICANT		
(USE SEVERAL SHEETS IF NECESTARY)	FILING DATE	GROUP Unknown 281	 1

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
(¥	4,109,030	8/78	Briska et al.			
()	4,624,046	11/86	Shideler et al.			
/ F	5,470,783	11/95	Chin et al.		_	
(F	5,502,09	03/96	Lin	-		

EXAMINER INITIAL		OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
F	1	"Extended Abstracts of the Electrochemical Society Meeting", Fall 1993, pp. 281-282
Œ	2	"High-Pressure Oxidation", VLSI Technology, 2d Edition, McGraw-Hill, Inc., New York, 1988, pp. 121-123
G	3	Wolf, Stanley, "Isolation Technologies for Integrated Circuits", Silicon Processing for the VLSI Era Vol. 2: Process Integration, Lattice Press, Sunset Beach, CA, 1990, pp. 17-27
8	4	Marshall, S. Et al., "Dry Pressure Local Oxidation of Silicon for IC Isolation", Journal of the Electrochemical Society (Oct. 1975), Vol. 122, No. 19, pp. 1411-1412

EXAMINER	Fourson	DATE CONSIDERED	5/4/99
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*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.

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